

Secure-International Data Encryption Algorithm

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Abstract: There are many security algorithms that are used for security purpose. IDEA is one of them. The block cipher IDEA operates with 64-bit plaintext and cipher text blocks and is controlled by a 128-bit key. The fundamental innovation in the design of this algorithm is the use of operations from three different algebraic groups. The algorithm structure has been chosen such that, with the exception that different key sub-blocks are used, the encryption process is identical to the decryption process. The drawback of IDEA is that the large numbers of weak keys were found in IDEA (International Data Encryption Algorithm). Also a new attack on round 6 of IDEA has been detected. In this paper we describe the design and implementation of secure data encryption algorithm(S-IDEA) protocol, the size of the key has been increased from 128 bits to 256 bits. This increased key size will increase the complexity of the algorithm. To increase the amount of diffusion two MA blocks (multiplicative additive block) are used in a single round of IDEA as compared to one MA block used previously in a single round, With these modifications in the proposed algorithm will increase the cryptographic strength.

Keywords: International data encryption algorithm(IDEA) ,Secure data encryption algorithm(S-IDEA).Multiple additive(MA)

I. INTRODUCTION

Symmetric encryption, also referred to as conventional encryption or single key encryption was the only type of encryption in use prior to the development of public-key encryption in 1970s. It remains by far the most widely used of the two types of the encryption[5].

A symmetric encryption scheme has five ingredients:

- Plaintext: This is the original intelligible message or data that is fed into the algorithm as input.
- Encryption algorithm: It performs various substitutions and transformations on the plaintext.
- Secret key: It is also an input to algorithm which is shared between the sender and receiver and kept secret between them. The exact substitutions and transformations performed by the algorithm depend on the key.
- Ciphertext: This is scrambled message produced as output. It depends on the plaintext and the secret key. The ciphertext is random stream of data and is unintelligible.
- Decryption algorithm: It is encryption algorithm run in reverse. It takes the ciphertext and secret key and produces the original plaintext.

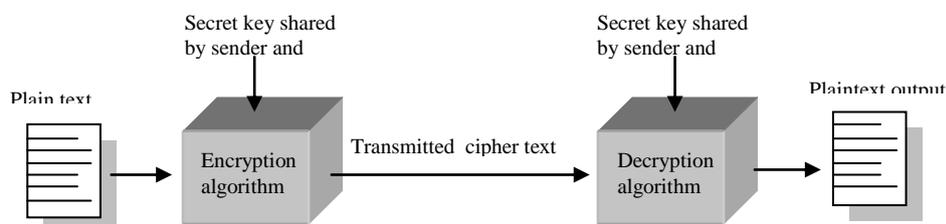


Fig.1 General Model of symmetric encryption[5]



DEFINITION 1.

A cryptosystem is a five -tuple (P, C, K, E, D), where the following conditions are satisfied:

1. P is a finite set of possible plain texts.
2. C is a finite set of possible ciphertexts.
3. K, the keyspace, is a finite set of possible keys.
4. For each $K \in k$, there is an encryption rule $e_K \in E$. and a corresponding decryption rule $d_K \in D$. Each $e_K : P \rightarrow C$ and $d_K : C \rightarrow P$ are functions such that $d_K(e_K(x)) = x$ for every plaintext $x \in P$.

The main property is property 4. It says that if a plaintext x is encrypted using eK, and the resulting cipher text is subsequently decrypted using dK, then the original plaintext x results.

Cryptography may be divide in two main categorries

- Asymmetric: For Encryption and Decryption using a pair of keys.
 - Symmetric: For Encryption and Decryption using the same key (or without key – in the case of Hash function)

All classical cryptosystems (that is cryptosystems that were developed before 1970s) are examples of symmetric keycryptosystems[6]. In addition, most modern cryptosystems are symmetric as well. Some of the most popular examples of modern symmetric key cryptosystems include AES (Advanced Encryption Standard), DES (Data Encryption Standard), IDEA, FEAL, RC5, and many others[2]. All symmetric key cryptosystems have a common property: they rely on a shared secret between communicating parties. This secret is used both as an encryption key and as a decryption key (thus the keyword "symmetric" in the name). This type of cryptography ensures only confidentiality and fails to provide the other objectives of cryptography. Even more importantly the disadvantage of symmetric key cryptography is that it cannot handle large communication networks[2]. If a node in a communication network of n nodes needs to communicate confidentially with all other nodes in the network, it needs n - 1 shared secrets. For large value of n this is highly impractical and inconvenient. On the other hand, an advantage over public key cryptosystems is that symmetric cryptosystems require much smaller key sizes for the same level of security[1]. Hence, the computations are much faster and the memory requirements are smaller (Whitfield et al, 1976)

In public key cryptosystems there are two different keys: a public key, which is publicly known, and the secret key, which is kept secret by the owner. The system is called "asymmetric" since the different keys are used for encryption and decryption– the public key and the private key[1]. If data is encrypted with a public key, it can only be decrypted using the corresponding private key, and vice versa[3]. Today, all public key cryptosystems rely on some computationally intractable problems. For example, the cryptosystem RSA relies on the difficulty of factoring large integers, while El-Gamal cryptosystem relies on the discrete logarithm problem (DLP), which is the problem of finding a logarithm of a group element with generator base in a finite Abelian group Public key cryptosystems do not need to have a shared secret between communicating parties. This solves the problem of large confidential communication network introduced earlier. In addition, public key cryptography opened door for ways of implementing technologies to ensure all goals of cryptography. By means of combining public key cryptography, public key certification, and secure hash functions, there are protocols that enable digital signatures, authentication, and data integrity[1]. Due to the increase in processor speed and even more due to smart modern cryptanalysis, the key size for public key cryptography grew very large. This created a disadvantage in comparison to symmetric key cryptosystems: public key cryptography is significantly slower[2], and requires large memory capacity and large computational power. As an example, a 128-bit key used with DES cryptosystem has approximately the same level of security as the 1024-bit key used with RSA public key cryptosystem (Oorschot P.C. van, et al, 1997).

II. BACKGROUND AND RELATED WORK

The cryptographic schemes are divided into two parts.

1. Classical Cryptography
2. Modern Cryptography

Classical ciphers are often divided into transposition ciphers and ciphers. A substitution cipher replaces the one symbol with another. if the symbol in the plain text are alphabetic character ,we replace one character with another. For example assume the statement to be encrypted is "this is the final chapter"[7].We apply substitution cipher and change each character by its next ASCII character, the resultant statement would be,"uijt jt uif gjomb....."This method is popularly known as Caesar cipher, as it was invited by Roman general Julius Carsar.Whan a character is substituted by another character, it is also known as monoalphabetic cipher.



transposition cipher changes the position of character. As appose of the substitution cipher, the transposition cipher change the order but not changes the character themselves. for example reshuffle the original string by sending the first character to 2nd position ,third character to 4th position ,the fifth character to 3rd position and so on[7].

Modern Cryptography

Public key cryptography(asymmetric)

(a) Encryption

- (i) Integer factorization: RSA and Rabin encryption.
- (ii) Discrete Logarithm: ElGamal Encryption.
- (iii) Elliptic Curve: Elliptic Curve Cryptosystem.
- (iv) Chaotic: Fractal Encryption.
- (v) Others: NTRU, Gang-Harn.

(b) Key Sharing

- (i) Discrete Logarithm: Diffie-Hellman.
- (ii) Elliptic Curve: ECDH.
- (iii) Chaotic: Fractal Key Exchange.

(c) Digital Signature

- (i) Integer factorization: RSA digital signature.
- (ii) Discrete Logarithm: DSA.
- (iii) Elliptic Curve: ECDSA.
- (iv) Chaotic: Fractal Digital signature.

Non public key

(a) Secret key (Symmetric)

- (i) Block cipher: DES
- (ii) Stream cipher.

(b) Hash function Secure Hash Algorithm (SHA).

The block cipher IDEA operates with 64-bit plaintext and cipher text blocks and is controlled by a 128-bit key. The fundamental innovation in the design of this algorithm is the use of operations from three different algebraic groups. The algorithm structure has been chosen such that, with the exception that different key sub-blocks are used, the encryption process is identical to the decryption process. The drawback of IDEA is that the large numbers of weak keys were found in IDEA (International Data Encryption Algorithm)[4]. Also a new attack on round 6 of IDEA has been detected. For this reason we have proposed secure data encryption algorithm(S-IDEA) protocol

III. SYSTEM DESIGN AND IMPLEMENTATION

(A) Encryption for S-IDEA

The encryption process consists of eight identical encryption steps (known as encryption rounds) followed by an output transformation. The structure of the first round is shown in detail.

In the first encryption round, the first four 16-bit key sub-blocks are combined with two of the 16-bit plaintext blocks using addition modulo 216, and with the other two plaintext blocks using multiplication modulo 216 + 1. The results are then processed further as shown in Figure3.4, whereby two more 16-bit key sub-blocks enter the calculation and the third algebraic group operator, the bit-by-bit exclusive OR, is used. At the end of the first encryption round four 16-bit values are produced which are used as input to the second encryption round in a partially changed order. The process described above for round one is repeated in each of the subsequent 7 encryption rounds using different 16-bit key sub-blocks for each combination. During the subsequent output transformation, the four 16-bit values produced at the end of the 8th encryption round are combined with the last four of the 104 key sub-blocks using addition modulo 216 and multiplication modulo 216 + 1 to form the resulting four 16-bit cipher text blocks.

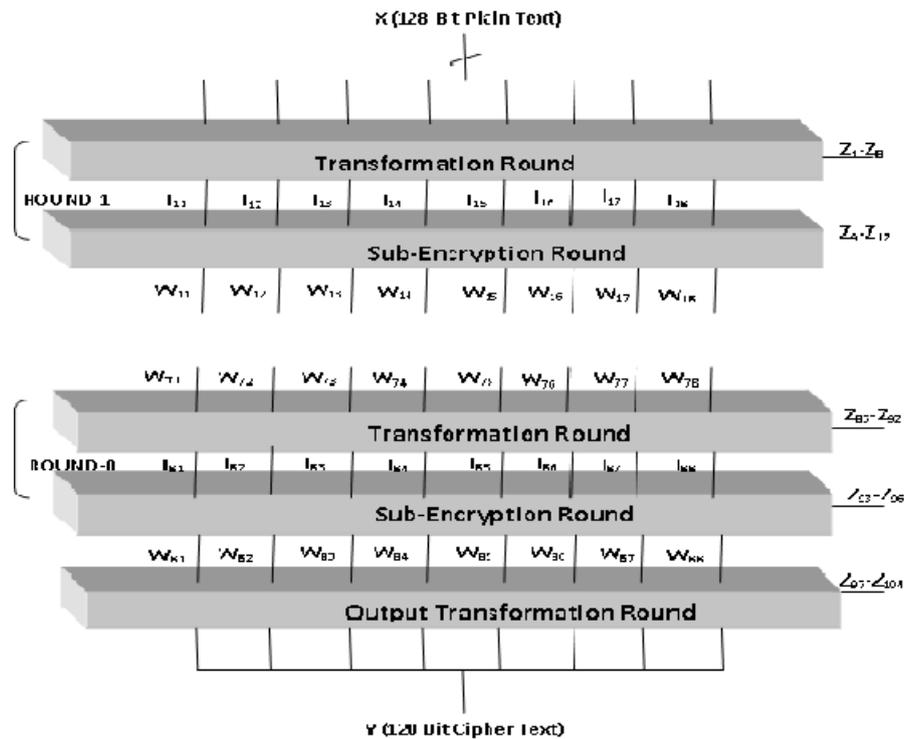


Fig 2: Block diagram of S-IDEA

Encryption algorithms for S-IDEA

1. Start
2. unsigned in[9], unsigned out[9], unsigned z[13][10]
3. unsigned i1, i2, i3, i4, i5, i6, i7, i8, kk, t1, t2, a, r;
4. int x;
5. i1=in [1], i2=in [2], i3=in [3], i4=in [4].
6. i5=in [5], i6=in [6], i7=in [7], i8=in [8].
7. for(r=1; r<=8; r++)
8. i1=mul(i1, z[1][r])
9. i2=(i2+z[2][r])&one
10. i3=(i3+z[3][r])&one
11. i4=mul(i4, z[4][r])
12. i5=mul(i5, z[5][r])
13. i6=(i6+z[6][r])&one
14. i7=(i7+z[7][r])&one
15. i8=mul(i8, z[8][r])
16. kk=mul(z[9][r], (i1^i3))
17. t1=mul(z[10][r], (kk+(i2^i4))&one)
18. t2=(kk+t1)&one
19. a=i2^t2
20. i1=i1^t1
21. i2=i3^t1
22. i3=a
23. i4=i4^t2
24. kk=mul(z[11][r], (i5^i7))
25. t1=mul(z[12][r], (kk+(i6^i8))&one)
26. t2=(kk+t1)&one
27. a=i6^t2
28. i5=i5^t1
29. i6=i7^t1
30. i7=a
31. i8=i8^t2
32. end

Detail of single round

- (1) The 128 bits text is processed in 8 block of 16 bit each.
- (2) The proposed modified version of IDEA(S-IDEA) can be seen as two sub-block of 64 bits running in parallel with each other. Each round in encryption uses two MA block and 12 keys.
- (3) Each round consists of two further divisions i.e. Transformation followed by Sub-Encryption, transformation in each round uses 8 keys whereas sub-encryption uses 4 keys.
- (4) The former description of keys is valid for round from 1 to 8 where as the 9th round called the Output transformation round uses 8 keys

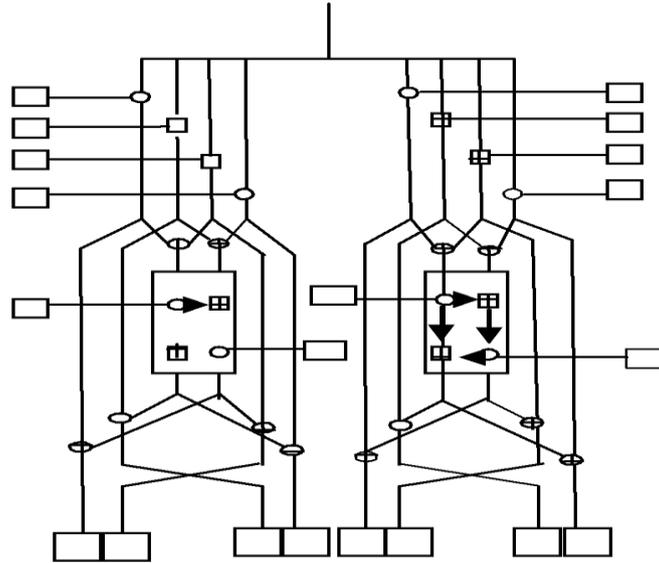


Fig 3. Structural details of round 1

From fig.1 and fig.3 following relations can be written:

$$\begin{aligned}
 W_{11} &= I_{11} \oplus MAR_1 (I_{11} \oplus I_{13}, I_{12} \oplus I_{14}) \\
 W_{12} &= I_{13} \oplus MAR_1 (I_{11} \oplus I_{13}, I_{12} \oplus I_{14}) \\
 W_{13} &= I_{12} \oplus MAL_1 (I_{11} \oplus I_{13}, I_{12} \oplus I_{14}) \\
 W_{14} &= I_{14} \oplus MAL_1 (I_{11} \oplus I_{13}, I_{12} \oplus I_{14}) \\
 W_{15} &= I_{15} \oplus MAR_2 (I_{15} \oplus I_{17}, I_{16} \oplus I_{18}) \\
 W_{16} &= I_{17} \oplus MAR_2 (I_{15} \oplus I_{17}, I_{16} \oplus I_{18}) \\
 W_{17} &= I_{16} \oplus MAL_2 (I_{15} \oplus I_{17}, I_{16} \oplus I_{18}) \\
 W_{18} &= I_{18} \oplus MAL_2 (I_{15} \oplus I_{17}, I_{16} \oplus I_{18}) \\
 W_{81} &= I_{81} \oplus MAR_1 (I_{81} \oplus I_{83}, I_{82} \oplus I_{84}) \\
 W_{82} &= I_{83} \oplus MAR_1 (I_{81} \oplus I_{83}, I_{82} \oplus I_{84}) \\
 W_{83} &= I_{82} \oplus MAL_1 (I_{81} \oplus I_{83}, I_{82} \oplus I_{84}) \\
 W_{84} &= I_{84} \oplus MAL_1 (I_{81} \oplus I_{83}, I_{82} \oplus I_{84}) \\
 W_{85} &= I_{85} \oplus MAR_2 (I_{85} \oplus I_{87}, I_{86} \oplus I_{88})
 \end{aligned}$$



$$W_{86}=I_{87} \oplus MAR_2 \quad (I_{85} \oplus I_{87}, I_{86} \oplus I_{88})$$

$$W_{87}=I_{86} \oplus MAL_2 \quad (I_{85} \oplus I_{87}, I_{86} \oplus I_{88})$$

$$W_{88}=I_{88} \oplus MAL_2 \quad (I_{85} \oplus I_{87}, I_{86} \oplus I_{88})$$

The following relations can be seen from fig.4:

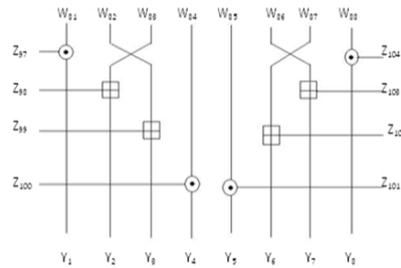


Fig 4. Output Transformation round

Table.1 Generation of encryption sub-keys

$$Y_1=W_{81} \odot Z_{97}$$

$$Y_2=W_{83} \boxplus Z_{98}$$

$$Y_3=W_{82} \boxplus Z_{99}$$

$$Y_4=W_{84} \odot Z_{100}$$

$$Y_5=W_{85} \odot Z_{101}$$

$$Y_6=W_{87} \boxplus Z_{102}$$

$$Y_7=W_{86} \boxplus Z_{103}$$

$$Y_8=W_{88} \odot Z_{104}$$

Algorithm of output transformation round

```

1. Start
2. out[1]=mul(i1,z[1][round+1])
3. out[2]=(i3+z[2][round+1])&one
4. out[3]=(i2+z[3][round+1])&one
5. out[4]=mul(i4,z[4][round+1])
6. out[5]=mul(i5,z[5][round+1])
7. out[6]=(i7+z[6][round+1])&one
8. out[7]=(i6+z[7][round+1])&one
9. out[8]=mul(i8,z[8][round+1])
10. for(x=1;x<9;x++)
11. out[x]
12. End
    
```

(B) Encryption sub key generation for S-IDEA:

The sample user key is fed into key generation for each round module which generates 16 sub-keys in each round. Sample user key is also fed into shift logic module that shifts the key by 25 bit circular left shift and again 16 sub-keys are generated. The process is carried out recursively till all 104 sub-keys are generated.

Table2 for encryption sub key

Round 1	Z ₁	Z ₂	Z ₃	Z ₄	Z ₅	Z ₆	Z ₇	Z ₈	Z ₉	Z ₁₀	Z ₁₁	Z ₁₂
Round 2	Z ₁₃	Z ₁₄	Z ₁₅	Z ₁₆	Z ₁₇	Z ₁₈	Z ₁₉	Z ₂₀	Z ₂₁	Z ₂₂	Z ₂₃	Z ₂₄
Round 3	Z ₂₅	Z ₂₆	Z ₂₇	Z ₂₈	Z ₂₉	Z ₃₀	Z ₃₁	Z ₃₂	Z ₃₃	Z ₃₄	Z ₃₅	Z ₃₆
Round 4	Z ₃₇	Z ₃₈	Z ₃₉	Z ₄₀	Z ₄₁	Z ₄₂	Z ₄₃	Z ₄₄	Z ₄₅	Z ₄₆	Z ₄₇	Z ₄₈
Round 5	Z ₄₉	Z ₅₀	Z ₅₁	Z ₅₂	Z ₅₃	Z ₅₄	Z ₅₅	Z ₅₆	Z ₅₇	Z ₅₈	Z ₅₉	Z ₆₀
Round 6	Z ₆₁	Z ₆₂	Z ₆₃	Z ₆₄	Z ₆₅	Z ₆₆	Z ₆₇	Z ₆₈	Z ₆₉	Z ₇₀	Z ₇₁	Z ₇₂
Round 7	Z ₇₃	Z ₇₄	Z ₇₅	Z ₇₆	Z ₇₇	Z ₇₈	Z ₇₉	Z ₈₀	Z ₈₁	Z ₈₂	Z ₈₃	Z ₈₄
Round 8	Z ₈₅	Z ₈₆	Z ₈₇	Z ₈₈	Z ₈₉	Z ₉₀	Z ₉₁	Z ₉₂	Z ₉₃	Z ₉₄	Z ₉₅	Z ₉₆
Round 9	Z ₉₇	Z ₉₈	Z ₉₉	Z ₁₀₀	Z ₁₀₁	Z ₁₀₂	Z ₁₀₃	Z ₁₀₄				

Algorithms for sub-key generation of S-IDEA

```

1. start
2. short unsigned
   userkey[17], unsigned z[13][10]
3. int i,r,j.
4. unsigned s[109]
5. for(i=1;i<17;i++)
6.   s[i-1]=userkey[i]
7.   for(i=16;i<109;i++)
8.     do if((i+2)%16==0)
9.       s[i]=(s[i-15]<<9)|(s[i-30]>>7)
10.    else if((i+1)%16==0)
11.      s[i]=(s[i-31]<<9)|(s[i-30]>>7)
12.    else
13.      s[i]=(s[i-15]<<9)|(s[i-14]>>7)
14. for(r=1;r<=round+1;r++)
15.   for(j=1;j<13;j++)
16.     z[j][r]=s[12*(r-1)+j-1]
17. for(j=1;j<13;j++)
18.   for(r=1;r<=round+1;r++)
19.     do if(r==9&&j>=9)
20.       Z[-][r]
21.     else
22.       z[j][r]
23. end

```

(c) Decryption of S-IDEA:

It implements all the 8+1 rounds of IDEA implementation with all functionalities (Additive modulo, Multiplicative modulo, Exclusive-OR, MA block etc.) of S-IDEA. Decryption. The decryption process is same as encryption process. The output of each round is denoted by V whereas the intermediate output undergoing transformation in each round denoted by J.

Relation between J and W

The output of transformation round of decryption (J) is related to output of sub-encryption round of encryption (W) and vice versa. Consider the fig.5. We can write the following relations:

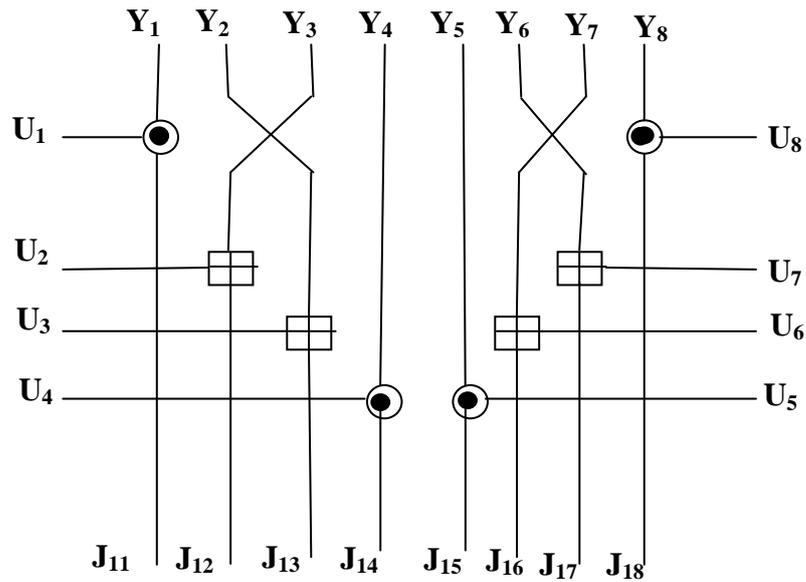


Fig 5. Decryption (transformation round) round 1

Table 2. Relation between encryption and decryption sub key

$$\begin{aligned}
 Y_1 &= W_{81} \odot Z_{97} \\
 Y_2 &= W_{83} \boxplus Z_{98} \\
 Y_3 &= W_{82} \boxplus Z_{99} \\
 Y_4 &= W_{84} \odot Z_{100} \\
 Y_5 &= W_{85} \odot Z_{101} \\
 Y_6 &= W_{87} \boxplus Z_{102} \\
 Y_7 &= W_{86} \boxplus Z_{103} \\
 Y_8 &= W_{88} \odot Z_{104}
 \end{aligned}$$

Substituting Y_1 in the equation corresponding to J_{11} :

$$\begin{aligned}
 J_{11} &= Y_1 \odot U_1 \\
 &= W_{81} \odot Z_{97} \odot U_1 \\
 &= W_{81} \odot Z_{97} \odot Z_{97}^{-1} \quad (U_1 = Z_{97}^{-1}) \\
 &= W_{81} \odot 1
 \end{aligned}$$

$$J_{11} = W_{81}$$

Substituting Y_2 in the equation corresponding to J_{12} :

$$\begin{aligned}
 J_{12} &= Y_2 \boxplus U_2 \\
 &= W_{83} \boxplus Z_{98} \boxplus U_2 \\
 &= W_{83} \boxplus Z_{98} \boxplus -Z_{98} \quad (U_2 = -Z_{98}) \\
 &= W_{83} \boxplus 0
 \end{aligned}$$

$$J_{12} = W_{83}$$

Similarly we can derive the following equivalences:

Table.3 Relation between J and W

$J_{11}=W_{81}$	$J_{15}=W_{85}$
$J_{12}=W_{83}$	$J_{16}=W_{87}$
$J_{13}=W_{82}$	$J_{17}=W_{86}$
$J_{14}=W_{84}$	$J_{18}=W_{88}$

Relation between V and I

We can write V in decryption corresponding to W in encryption as follows:

$$\begin{aligned}
 V_{11} &= J_{11} \oplus MA_{R1}(J_{11} \oplus J_{13}, J_{12} \oplus J_{14}) \\
 V_{12} &= J_{13} \oplus MA_{R1}(J_{11} \oplus J_{13}, J_{12} \oplus J_{14}) \\
 V_{13} &= J_{12} \oplus MA_{L1}(J_{11} \oplus J_{13}, J_{12} \oplus J_{14}) \\
 V_{14} &= J_{14} \oplus MA_{L1}(J_{11} \oplus J_{13}, J_{12} \oplus J_{14}) \\
 V_{15} &= J_{15} \oplus MA_{R2}(J_{15} \oplus J_{17}, J_{16} \oplus J_{18}) \\
 V_{16} &= J_{17} \oplus MA_{R2}(J_{15} \oplus J_{17}, J_{16} \oplus J_{18}) \\
 V_{17} &= J_{16} \oplus MA_{L2}(J_{15} \oplus J_{17}, J_{16} \oplus J_{18}) \\
 V_{18} &= J_{18} \oplus MA_{L2}(J_{15} \oplus J_{17}, J_{16} \oplus J_{18})
 \end{aligned}$$

Consider the following equation:

$$V_{15} = J_{15} \oplus MA_{R2}(J_{15} \oplus J_{17}, J_{16} \oplus J_{18})$$

Substituting the values of $J_{15}, J_{16}, J_{17}, J_{18}$:

$$V_{15} = W_{85} \oplus MA_{R2}(W_{85} \oplus W_{86}, W_{87} \oplus W_{88})$$

Substituting the values of $W_{85}, W_{86}, W_{87}, W_{88}$:

$$\begin{aligned}
 V_{15} &= [I_{85} \oplus MA_{R2}(I_{85} \oplus I_{87}, I_{86} \oplus I_{88})] \oplus \\
 &MA_{R2} [I_{85} \oplus MA_{R2}(I_{85} \oplus I_{87}, I_{86} \oplus I_{88}) \\
 &\oplus I_{87} \oplus MA_{R2}(I_{85} \oplus I_{87}, I_{86} \oplus I_{88}), \\
 &I_{86} \oplus MA_{L2}(I_{85} \oplus I_{87}, I_{86} \oplus I_{88}) \\
 &\oplus I_{88} \oplus MA_{L2}(I_{85} \oplus I_{87}, I_{86} \oplus I_{88})]
 \end{aligned}$$

$$\begin{aligned}
 V_{15} &= [I_{85} \oplus MA_{R2}(I_{85} \oplus I_{87}, I_{86} \oplus I_{88})] \\
 &\oplus MA_{R2} [I_{85} \oplus I_{87}, I_{86} \oplus I_{88}]
 \end{aligned}$$

$$V_{15} = I_{85} \oplus 0$$

$$V_{15} = I_{85}$$

Similarly we can derive the following equivalences:

Table.4 Relation between V and I

$V_{11}=I_{81}$	$V_{15}=I_{85}$
$V_{12}=I_{83}$	$V_{16}=I_{87}$
$V_{13}=I_{82}$	$V_{17}=I_{86}$
$V_{14}=I_{84}$	$V_{18}=I_{88}$

Algorithms for S-IDEA Decryption:

```

1. Start
2.   Unsigned      in[9],unsigned
   out[9],unsigned dk[13][10]
3.   unsigned
   i1,i2,i3,i4,i5,i6,i7,i8,kk,t1,t2,a,r
4. int x
5. i1=in[1] i2=in[2] i3=in[3] i4=in[4]
6. i5=in[5] i6=in[6] i7=in[7] i8=in[8]
7.for(r=1;r<=8;r++)
8. i1=mul(i1,dk[1][r])
9.i2=(i2+dk[2][r])&one
10.i3=(i3+dk[3][r])&one
11. i4=mul(i4,dk[4][r])
12. i5=mul(i5,dk[5][r])
13.i6=(i6+dk[6][r])&one
14.i7=(i7+dk[7][r])&one
15. i8=mul(i8,dk[8][r])
17.kk=mul(dk[9][r),(i1^i3))
18.t1=mul(dk[10][r),(kk+(i2^i4))&one)
19. t2=(kk+t1)&one
20.a=i2^t2; i1=i1^t1
21.i2=i3^t1; i3=a; i4=i4^t2
22.kk=mul(dk[11][r),(i5^i7))
23. t1=mul(dk[12][r),(kk+(i6^i8))&one)
24. t2=(kk+t1)&one
25. a=i6^t2; i5=i5^t1
26. i6=i7^t1; i7=a; i8=i8^t2
27. End

```

Table 5. Relation between encryption and decryption sub key

Round 1

De.Sub-keys	U ₁	U ₂	U ₃	U ₄	U ₅	U ₆	U ₇	U ₈	U ₉	U ₁₀	U ₁₁	U ₁₂
Equivalent to	Z ₉₇ ⁻¹	-Z ₉₈	-Z ₉₉	Z ₁₀₀ ⁻¹	Z ₁₀₁ ⁻¹	-Z ₁₀₂	-Z ₁₀₃	Z ₁₀₄ ⁻¹	Z ₉₃	Z ₉₄	Z ₉₅	Z ₉₆

Round 2

De.Sub-keys	U ₁₃	U ₁₄	U ₁₅	U ₁₆	U ₁₇	U ₁₈	U ₁₉	U ₂₀	U ₂₁	U ₂₂	U ₂₃	U ₂₄
Equivalent to	Z ₈₅ ⁻¹	-Z ₈₇	-Z ₈₆	Z ₈₈ ⁻¹	Z ₈₉ ⁻¹	-Z ₉₁	-Z ₉₀	Z ₉₂ ⁻¹	Z ₈₁	Z ₈₂	Z ₈₃	Z ₈₄

Round 3



De.Sub-keys	U ₂₅	U ₂₆	U ₂₇	U ₂₈	U ₂₉	U ₃₀	U ₃₁	U ₃₂	U ₃₃	U ₃₄	U ₃₅	U ₃₆
Equivalent to	Z ₇₃ ⁻¹	-Z ₇₅	-Z ₇₄	Z ₇₆ ⁻¹	Z ₇₇ ⁻¹	-Z ₇₉	-Z ₇₈	Z ₈₀ ⁻¹	Z ₆₉	Z ₇₀	Z ₇₁	Z ₇₂

Round 4

De.Sub-keys	U ₃₇	U ₃₈	U ₃₉	U ₄₀	U ₄₁	U ₄₂	U ₄₃	U ₄₄	U ₄₅	U ₄₆	U ₄₇	U ₄₈
Equivalent to	Z ₆₁ ⁻¹	-Z ₆₃	-Z ₆₂	Z ₆₄ ⁻¹	Z ₆₅ ⁻¹	-Z ₆₇	-Z ₆₆	Z ₆₈ ⁻¹	Z ₅₇	Z ₅₈	Z ₅₉	Z ₆₀

Round 5

De.Sub-keys	U ₄₉	U ₅₀	U ₅₁	U ₅₂	U ₅₃	U ₅₄	U ₅₅	U ₅₆	U ₅₇	U ₅₈	U ₅₉	U ₆₀
Equivalent to	Z ₄₉ ⁻¹	-Z ₅₁	-Z ₅₀	Z ₅₂ ⁻¹	Z ₅₃ ⁻¹	-Z ₅₅	-Z ₅₄	Z ₅₆ ⁻¹	Z ₄₅	Z ₄₆	Z ₄₇	Z ₄₈

Round 6

De.Sub-keys	U ₆₁	U ₆₂	U ₆₃	U ₆₄	U ₆₅	U ₆₆	U ₆₇	U ₆₈	U ₆₉	U ₇₀	U ₇₁	U ₇₂
Equivalent to	Z ₃₇ ⁻¹	-Z ₃₉	-Z ₃₈	Z ₄₀ ⁻¹	Z ₄₁ ⁻¹	-Z ₄₃	-Z ₄₂	Z ₄₄ ⁻¹	Z ₃₃	Z ₃₄	Z ₃₅	Z ₃₆

Round 7

De.Sub-keys	U ₇₃	U ₇₄	U ₇₅	U ₇₆	U ₇₇	U ₇₈	U ₇₉	U ₈₀	U ₈₁	U ₈₂	U ₈₃	U ₈₄
Equivalent to	Z ₂₅ ⁻¹	-Z ₂₇	-Z ₂₆	Z ₂₈ ⁻¹	Z ₂₉ ⁻¹	-Z ₃₁	-Z ₃₀	Z ₃₂ ⁻¹	Z ₂₁	Z ₂₂	Z ₂₃	Z ₂₄

Round 8

De.Sub-keys	U ₈₅	U ₈₆	U ₈₇	U ₈₈	U ₈₉	U ₉₀	U ₉₁	U ₉₂	U ₉₃	U ₉₄	U ₉₅	U ₉₆
Equivalent to	Z ₁₃ ⁻¹	-Z ₁₅	-Z ₁₄	Z ₁₆ ⁻¹	Z ₁₇ ⁻¹	-Z ₁₉	-Z ₁₈	Z ₂₀ ⁻¹	Z ₉	Z ₁₀	Z ₁₁	Z ₁₂

Round 9

De.Sub-keys	U ₉₇	U ₄₈	U ₉₉	U ₁₀₀	U ₁₀₁	U ₁₀₂	U ₁₀₃	U ₁₀₄				
Equivalent to	Z ₁ ⁻¹	-Z ₂	-Z ₃	Z ₄ ⁻¹	Z ₅ ⁻¹	-Z ₆	-Z ₇	Z ₈ ⁻¹				

IV. EXPERIMENTAL RESULT

The result displays in fig. 6 shows the sample user key that is generated in the proposed algorithm. From the user key all 104 sub-keys are generated by the algorithm. It also displays the sample user data generated in the algorithm which has to be encrypted using S-IDEA.

Fig.7 depicts 104 encryption sub-keys generated by the proposed algorithm from sample user key. It also shows which sub-keys will be used in which encryption round.

Fig. 8 shows the progress of S-IDEA Encryption process. It shows the data encrypted after each round and finally the cipher text is given as the output

Fig 9, shows the 104 decryption sub-keys generated by the algorithm from encryption sub-keys. It also shows which sub-keys will be used in which decryption round.

Fig 10 ,shows the progress of S-IDEA Decryption process. It shows the data decrypted after each round and finally the plain text is given as the output.

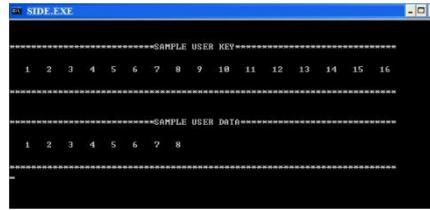


Fig 6. Simple user data encrypted by S-IDEA

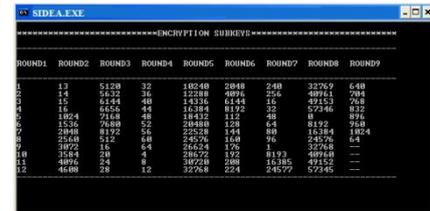


Fig 7. Encryption sub-keys generated by S-IDEA

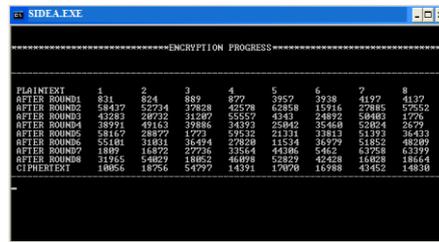


Fig 8. S-IDEA Decryption process

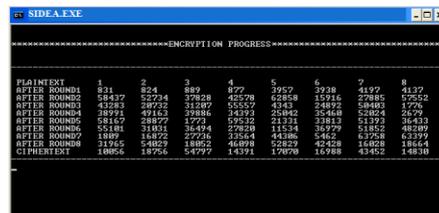


Fig 9. Decryption sub key generated by S-IDEA

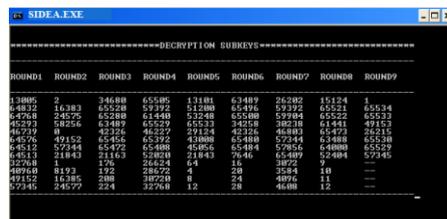


Fig 10. S-IDEA Decryption process

V. CONCLUSION AND FUTURE WORK

The basic aim of this paper is to increase the strength of existing IDEA algorithm. The proposed algorithm International data Encryption algorithm (S-IDEA) has two key features:-increased key size (256 bits) and increased degree of diffusion (two MA blocks are used in a single round instead of one). The 104 sub-keys are being used as compared to 52 sub-keys previously which enhance the complexity of confusion. Therefore the probability of other forms of attack is reduced due to amount of work that has to be carried out when 104 sub-keys are involved. Addition of a new MA block in each round of S-IDEA has contributed to an increase in complexity of diffusion. It makes the algorithm more secure and less susceptible to cryptanalysis.



The proposed algorithms increased the cryptographic strength and eliminate the shortcoming of the existing International data Encryption algorithm (IDEA). The future scope of S-IDEA algorithm is that it can also be implemented in hardware using VLSI technology.

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Biography



Harivans pratap singh received the B.Tech. degree in computer science from the Faculty of Engineering and Technology, R.B.S College Agra, India and he is currently working toward the M.Tech. degree in the Department of Computer Science, Uttarakhand technical University. He is an Assistant Professor, Department of Information Technology, Galgotia’s college of engineering and technology Mahamaya technical University. His research area include information systems security and privacy.



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He had supervised 6 Ph.D and currently guiding five research scholars. He had authored four books in the area of Computer Network and Security and published and presented 60 research papers in international journals and international conferences and wrote more than 10 articles on various topics in national magazines. He is recipient of Young Scientist Award in IIIrd Uttarakhand State Science Congress & Ist Uttaranchal State Science Congress organized by Uttaranchal Council for Science & Technology, Department of Science & Technology, Govt. of Uttarkhand, India (10,11 Nov 2008 & 11 Nov 2006). He is Member of Institution of Engineers India (IEI) and ISTE.