



# **Improvement of Voltage Stability in Power System by Using SVC and STATCOM**

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**ABSTRACT:** The increase in power demand has forced the power system to operate closer to its stability limit. Voltage instability and line overloading have become challenging problems due to the strengthening of power system by various means. The nature of voltage stability can be analysed by the production, transmission and consumption of reactive power. One of the major causes of voltage instability is the reactive power unbalancing which occurs in stressed condition of power system. Flexible AC transmission system (FACTS) devices play an important role in improving the performance of a power system, but these devices are very costly and hence need to be placed optimally in power system. FACTS device like static var compensator (SVC) and thyristor controlled series compensator (TCSC) can be employed to reduce the flows in heavily loaded lines, resulting in a low system loss and improved stability of network. In this paper, a method based on line stability index, real power performance index and reduction of total system VAR power losses has been proposed to decide the optimal location of TCSC.

Now a day, Voltage Stability has become a major concern in power systems planning and operation. This problem has become very complex due to the continuous growing on system interconnections and demand for electricity and also due to economical and environmental constraints to properly expand the system. This has increased the importance of implementing suitable and efficient techniques for improving voltage stability of stressed power system. FACTS are one aspect of the power electronics revolution that is taking place in all areas of electric energy.

**KEYWORDS:** Flexible AC Transmission System (FACTS), Static Var Compensator (SVC), PSAT, Voltage Stability, Voltage Stability Index (VSI).

## **I.INTRODUCTION**

Most of the large power system blackouts, which occurred worldwide over the last twenty years, which are caused by heavily stressed system with large amount of real and reactive power demand and low voltage condition. When the voltages at power system buses are low, the losses will also to be increased. This study is devoted to develop a technique for improving the voltage and eliminate voltage instability in a power system. Application of Flexible AC Transmission System (FACTS) devices are currently pursued very intensively to achieve better control over the transmission lines for manipulating power flows. They can provide direct and flexible control of power transfer and are very helpful in the operation of power network. The power system performance and the power system stability can be enhanced by using FACTS device [1].

FACTS devices, which are power electronic based devices can change parameters like impedance, voltage and phase angle. They also helps to reduce flows in heavily loaded lines, resulting in an increased load ability, low system loss, improved stability of the network, reduced cost of production and fulfilled contractual requirement by controlling the power flows in the network. They provide control facilities, both in steady state power flow control and dynamic stability control [2]. The possibility of controlling Power flow in an electrical power system without generation rescheduling or topological changes can improve the performance considerably. Static Var Compensator (SVC) is one of the most effective device for enhancing the power stability and power transfer capability of transmission network, for this the SVC should be properly installed in the system with appropriate parameter setting. The some factors considering for optimal installation and the optimal parameter of SVC, which are the stability margin improvement, power loss reduction, power blackout prevention and the power transmission capacity enhancement [3].

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This paper presents the method of the optimal location of SVC for enhancing the voltage stability of the power system using VSI. The SVC has been tested on the 9-Bus test System and the solution for optimal location of this device will obtain and discuss in this section.

Shravana Musunuri, Gholamreza Dehnavi [1], compares STATCOM, SVC, SSSC and TCSC devices for steady state voltage stability improvement. The choice of the location, sizing and cost comparison of these FACTS devices with traditional reactive power/voltage stability devices are presented. Bhavin. M. Patel [2], explained the enhancement of steady state voltage stability using SVC and TCSC. The results presented in this paper clearly show how SVC and TCSC can be used to increase system stability in practical power systems with the use of simulink model. Rambabu, Y.P.Obulesu, Saibabu [3], aimed for improvement of voltage profile and reduce power system losses by using multi type FACTS devices. Their individual contribution towards the improvement of voltage profile and reduction of power losses has been tested on a 5-bus system. P. Ajay D. Vimal Raj, M. Sudhakaran [4], proposed optimum load shedding in power system strategies with voltage stability indicators. In this paper a fast method for determining the location and quantity of the load to be shed in order to avoid risk of voltage instability is presented. P. Kessel and H. Glavitsch [5], estimate the voltage stability of a power system. This method is based on the indicators of risk of voltage instability. This paper proposes a method to compute load curtailment evaluation, using optimal power flow computation, incorporating the steady state voltage stability margin constraints. N.Boonpirom and K.Paitoon Wattanakij [6], presented the static voltage stability enhancement using FACTS devices. With the insertion of SVC, enhancement in voltage at various buses, reduction in system power loss, improvement in power factor and power flow are the objectives of the study. Mehrdad Ahmad, Mostafa Alinezhad, [7] Compares SVC and STATCOM for the static voltage stability margin enhancement, The choice of the location and sizing of these devices is also presented and an analysis is made on the IEEE 14 bus system using Power System Analysis Toolkit (PSAT) software.

## II. MATHEMATICAL MODEL OF SVC

In this paper steady state model of FACTS devices are developed for power flow studies. SVC modelled is incorporated into the bus as shunt element of transmission line. Mathematical model for SVC is implemented by MATLAB programming language.

### Static VAR compensator (SVC)

The SVC uses conventional thyristor to achieve fast control of shunt-connected capacitors and reactors. The configuration of the SVC is shown in Fig.1, which basically consists of a fixed capacitor (C) and a thyristor controlled reactor (L). The firing angle control of the thyristor banks determines the equivalent shunt admittance presented to the power system.

A shunt connected static VAR generator or absorber whose output is adjusted to exchange capacitive or inductive current so as to maintain or control bus voltage of the electrical power system. Variable shunt susceptance model of SVC [2] is shown in Fig.1

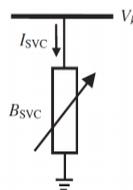


Fig 1: Variable shunt susceptance model

As far as steady state analysis is concerned, both configurations can be modelled along similar lines. The SVC structure shown in Fig. 1 is used to derive a SVC model that considers the Thyristor Controlled Reactor (TCR) firing angle as a state variable. This is a new and more advanced SVC representation than those currently available. The SVC is treated as a generator behind an inductive reactance when the SVC is operating within the limits. The reactance represents the SVC voltage regulation characteristic. The reason for including the SVC voltage current slope in power flow studies is compelling. The slope can be represented by connecting the SVC models to an auxiliary bus coupled to the high voltage bus by an inductive reactance consisting of the transformer reactance and the SVC slope, in per unit (p.u) on the SVC base. A simpler representation assumes that the SVC slope, accounting for voltage regulation is zero. This

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assumption may be acceptable as long as the SVC is operating within the limits, but may lead to gross errors if the SVC is operating close to its reactive limits [3].

The current drawn by the SVC is,  $I_{SVC} = jB_{SVC} V_K$

The reactive power drawn by SVC, which is also the reactive power injected at bus k is,

$$Q_{SVC} = Q_K = -V_K^2 B_{SVC}$$

Where,  $V_K$  – voltage at bus k

$B_{SVC}$  – Susceptance

$Q_{SVC}$  – Reactive power drawn by SVC

### III. OPTIMAL LOCATION OF FACTS DEVICES

To improve the voltage profile and voltage stability of a power system an alternative solution is to locate an appropriate Flexible AC transmission system (FACTS) device [2]. FACTS devices are the solid state converters having capability of improving power transmission capacity, improving voltage profile, enhancing power system stability, minimizing transmission losses etc. In order to optimize and to obtain the maximum benefits from their use, the main issues to be considered are the type of FACTS devices, the settings of FACTS devices and optimal location of FACTS devices.

The optimal location of SVC has been selected on the basis of voltage stability index (VSI) for improvement of voltage stability of power system [6, 7]. Voltage stability index can be used for determining the weakest line in a power system. This voltage stability index considers both active and reactive powers to evaluate voltage stability, which provides information about the stability condition of the lines and also determines the weakest line in the system.

### IV. VOLTAGE STABILITY INDEX

Voltage stability is becoming an increasing source of concern in secure operating of present-day power systems. The problem of voltage instability is mainly considered as the inability of the network to meet the load demand imposed in terms of inadequate reactive power support or active power transmission capability or both. It is mainly concerned with the analysis and the enhancement of steady state voltage stability based on L-index. This L-Index [3] determines how any system is close to its instability limit.

Consider, a simple system consisting of sending and receiving end buses as shown,

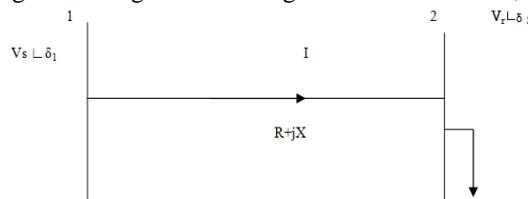


Fig 2: Single-line model of a simple system

Where:  $V_S, V_R$  = sending and receiving voltages at system buses

$\delta_1, \delta_2$  = sending and receiving voltages angle at system bus 1 and 2

$P, Q$  = real and reactive power at buses

$R + jX$  = line impedance between bus 1 and 2.

Current flowing through the branch,  $I = \frac{V_s \angle \delta_1 - V_r \angle \delta_2}{R + jX}$

$$P - jQ = V_r^* I$$

$$P - jQ = \frac{V_s V_r \angle (\delta_1 - \delta_2) - V_r^2}{R + jX}$$

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$$(P - jQ)(R + jX) = V_s V_r \angle(\delta_1 - \delta_2) - V_r^2$$

The real term of above equation is,

$$V_s V_r \cos(\delta_1 - \delta_2) = V_r^2 + (RP + XQ) \dots \dots \dots (1)$$

And imaginary part is,  $V_s V_r \sin(\delta_1 - \delta_2) = XP - RQ \dots \dots \dots (2)$

Squaring and adding the above two terms, we get

$$V_r^4 + V_r^2(2RP + 2XQ - V_s^2) + (P^2 + Q^2)(R^2 + X^2) = 0$$

The above equation is a quadratic equation of  $V_r^2$

$$b^2 - 4ac \geq 0, \text{ i.e.}$$

$$\left[2(RP + XQ) - V_s^2\right]^2 - 4(P^2 + Q^2)(R^2 + X^2) \geq 0$$

After simplifying the above equation, we get the equation for the stability index can be written as,

$$L_{(P,V)} = 4 \left\{ \left( \frac{PX - RQ}{V_s^2} \right)^2 + \left( \frac{RP + XQ}{V_s^2} \right)^2 \right\}$$

Where L = stability index.

For stable system,  $L < 1$ .

It can be seen that when a load bus approaches a steady state voltage collapse situation, the L-index approaches the numerical value 1.0. Hence for an overall system voltage stability condition, the index evaluated at any of the buses must be less than unity. Thus the index value L gives an indication of how far the system is from voltage collapse [6].

## V. RESULT AND DISCUSSION

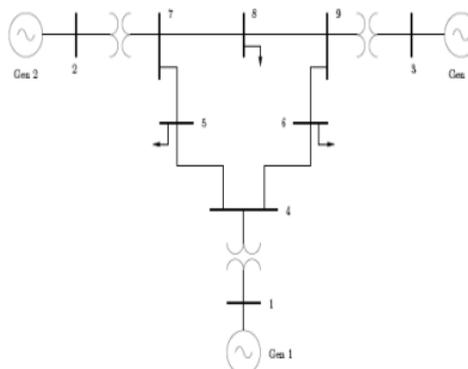
For the validation of the proposed FACT's devices, SVC have been tested on the WSCC 9-Bus test System. A MATLAB code for both techniques was developed for simulation purpose.

An WSCC 9 bus test system and this test system including 9 buses ,3 generators ,6 lines ,3 transformers and 3 loads is simulated using PSAT .and used to try models and control strategies is presented. The generators are modelled as standard PV buses with both P and Q limits; loads are represented as constant PQ loads. The P and Q load powers are not voltage dependent and are assumed to change as follows

$$P_L = P_{L0}(1 + \lambda)$$

$$Q_L = Q_{L0}(1 + \lambda)$$

Where  $P_{L0}$  and  $Q_{L0}$  are the active and reactive base loads. Whereas  $P_L$  and  $Q_L$  are active and reactive loads at bus L for the current operating point as defined by  $\lambda$ .





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## Line Data

Line	R (p.u.)	X (p.u.)	B (p.u.)
9-8	0.0119	0.1008	0.2090
7-8	0.0085	0.0720	0.1490
9-6	0.0390	0.1700	0.3580
7-5	0.0320	0.1610	0.3060
5-4	0.0100	0.0850	0.1760
6-4	0.0170	0.0920	0.1580
2-7	0.000	0.0625	0.0000
3-9	0.000	0.0586	0.0000
1-4	0.000	0.0576	0.0000

Line	P (p.u.)	Q (p.u.)	L-Index
9-8	0.24183	0.0312	0.02463
7-8	0.7638	-0.00797	0.03353
9-6	0.60817	-0.18075	0.01660
7-5	0.8662	-0.08381	<b>0.12752</b>
5-4	-0.4068	-0.38687	-0.1452
6-4	-0.3053	-0.16543	-0.0771
2-7	1.63	0.06654	0.0534
3-9	0.85	-0.1086	-0.0152
1-4	0.7164	0.27046	0.0634

Table 1: Line data for 9-bus system

Table 2: Power flow and Voltage Stability Index for 9bus test system

## Location of SVC

Voltage stability indices are calculated for the WSCC 9 bus system without SVC as shown in table 2. By considering the Voltage stability index (L-Index) value, it is observed that line 7-5 is more sensitive towards system security. Therefore it is more suitable location for SVC to improve power system security/stability. After placement of SVC voltage stability is improved, the analyses of voltage magnitudes for WSCC 9 bus test system without and with SVC as shown in table 3.

Bus	Before Placement of SVC	After Placement of SVC
	VM (p.u.)	VM (p.u.)
1	1.038	1.040
2	0.858	1.025
3	0.960	1.025
4	0.924	1.026
5	0.789	0.997
6	0.862	1.014
7	0.825	1.029
8	0.819	1.025
9	0.910	1.035

Table 3: Analysis of Voltage magnitudes for 9 bus test system without and with SVC

## VI. SIMULATION RESULTS

### Base case

The continuation power flow analysis using PSAT [12] is run for the test system and the voltage profile without SVC is obtained. The voltage profile and PV curve for the bus 5, bus 6 and bus 7. It can be seen from the figure 4 (a) that the maximum loading parameter  $\lambda = 1.8708$  p.u for the base case.

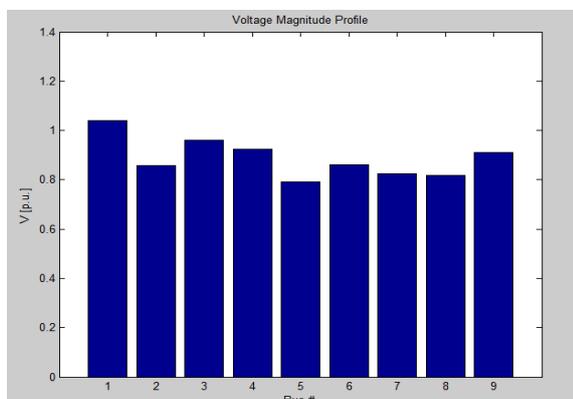


Fig 4: (a) Voltage profile for the base case without SVC

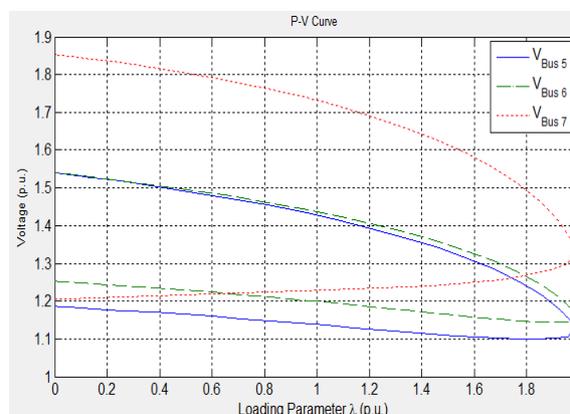


Fig 4: (b) P-V Curve without SVC

## With SVC

It is seen from Fig. 4(b) that the voltage collapse occurs at the maximum loading of  $\lambda = 1.8708$  and line 7-5 is the best location for shunt compensation. As described in the earlier section, the SVC was placed in the weakest bus lines one at a time and it was found that for this system bus line 7-5 shunt compensation has better results. Fig.5 (a) gives the voltage profile for the system with SVC. It is seen that the maximum loading factor  $\lambda = 1.9815$  has increases when compared to the base case.

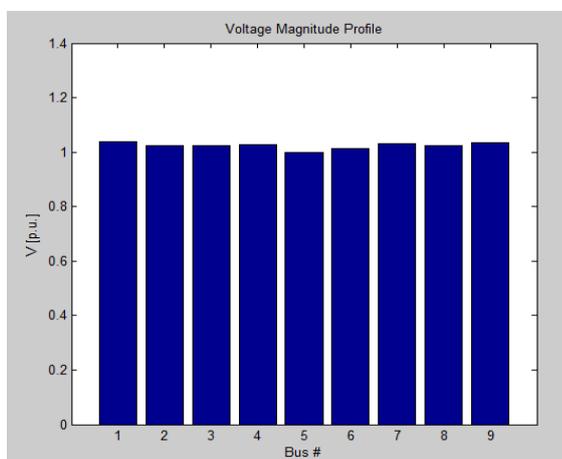


Fig 5: (a) Voltage profile with SVC

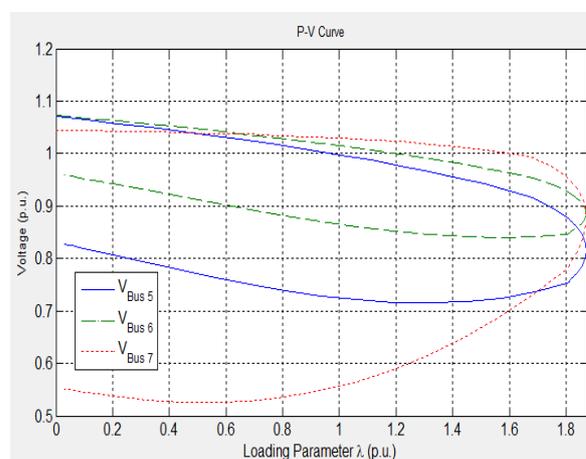


Fig 5: (b) P-V curve with SVC

## VII. CONCLUSION

In this paper, a new method for optimal placement and parameters settings of SVC has been proposed for improving voltage profile in a power system. The proposed approach has been implemented on WSCC 9-bus system. The criterion for selection of optimal placement of SVC was to maintain the voltage profile, minimize the voltage deviations and to reduce the power losses using VSI. Simulations performed on the test system shows that the optimally placed SVC maintains the voltage profile, improve the stability and effectively increases the efficiency of power system.

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